

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S): HSU, et al.

EMARK OFFICE

DOCKET: 728-208 (YOR9-20014-0270 US)

PATENT

PA

09/880,223 SERIAL NO.:

FILED:

June 13, 2001

FOR: T-RAM CELL HAVING A BURIED VERTICAL THYRISTOR AND A PSEUDO-TFT TRANSFER GATE AND A METHOD FOR FABRICATING

THE SAME

Assistant Commissioner for Patents Washington, DC 20231

AMENDMENT TRANSMITTAL FORM

Sir:

Transmitted herewith is an amendment in the above-identified application.

- [] Small entity status of this application under 37 C.F.R. §§1.9 and 1.27 has been established by a verified statement previously submitted.
- [] A verified statement to establish small entity under 37 C.F.R. §§1.9 and 1.27 is enclosed.
- [X] No additional fee is required.

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	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NO. PREVIOUSLY PAID FOR		PRESENT EXTRA				ADD I	ADDIT. FEE OR		•	ADDIT. FEE	
TOTAL	*8	MINUS	**28	=		0	х	9	\$		х	18	\$	0
INDEP.	*1	MINUS	***3	=		0	х	42	\$		х	84	\$	0
□ FIRST	PRESEN	TATION OF	MULTIPLE	DEP.	CLAIM		Х	140	\$		Х	280	\$	
								TOT	AL:	\$	OR ADD I T	TOTAL		

- [] Please charge Deposit Account No. $\underline{04\text{-}1121}$ in the amount of \$____. Two (2) copies of this sheet are enclosed.
- [] A check in the amount of \$____is enclosed.
- [X] Please charge any deficiency as well as any other fee(s) which may become due under 37 C.F.R. §§1.16 and/or 1.17 at any time during the pendency of this application, or credit any overpayment of such fee(s) to Deposit Account No. $\underline{04\text{-}1121}$. Also, in the event any extensions of time for responding are required for the pending application(s), please treat this paper as a petition to extend the time as required and charge Deposit Account No. 04-1121 therefor. TWO (2) COPIES OF THIS SHEET ARE ENCLOSED.

DILWORTH & BARRESE, LLP 333 Earle Ovington Blvd. Uniondale, NY 11553 (516) 228-8484

Reg Not 33,494 Attorney for Applicant

CERTIFICATE OF MAILING UNDER 37 C.F.R. \$1.8(a)

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Dated: February 11, 2003

^{*} If the entry in Col. 1 is less than entry in Col. 2, write "0" in Col. 3.
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RECEIVE US Patent Application No. 09 Attorney Docket No.: 728-208 (YOR9-2001-0270 US)

FEB 20 2003 IN THE UNITED STATES PA TENT AND TRADEMARK OFFICE ECHAULUGT LENTER 2800

APPLICANTS:

HSU, et al.

GROUP ART UNIT: 2814

APPLN NO:

09/880,223

EXAMINER: Rao, Shrinivas

FILING DATE:

June 13, 2001

DATED: February 11, 2003

FOR:

T-RAM CELL HAVING A

BURIED VERTICAL THYRISTOR AND A PSEUDO-TFT TRANSFER **GATE AND A METHOD FOR FABRICATING THE SAME**

Assistant Commissioner for Patents Washington, DC 20231

RESPONSE

In response to the Office Action of the United States Patent and Trademark Office dated December 19, 2002, please amend this application as follows:

IN THE CLAIMS

Please add the following new claims:

29. (New) The array according to claim 13, wherein the transfer gate comprises a horizontally stacked pseudo-TFT transfer gate.

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8 (a)

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